

FIG. 1

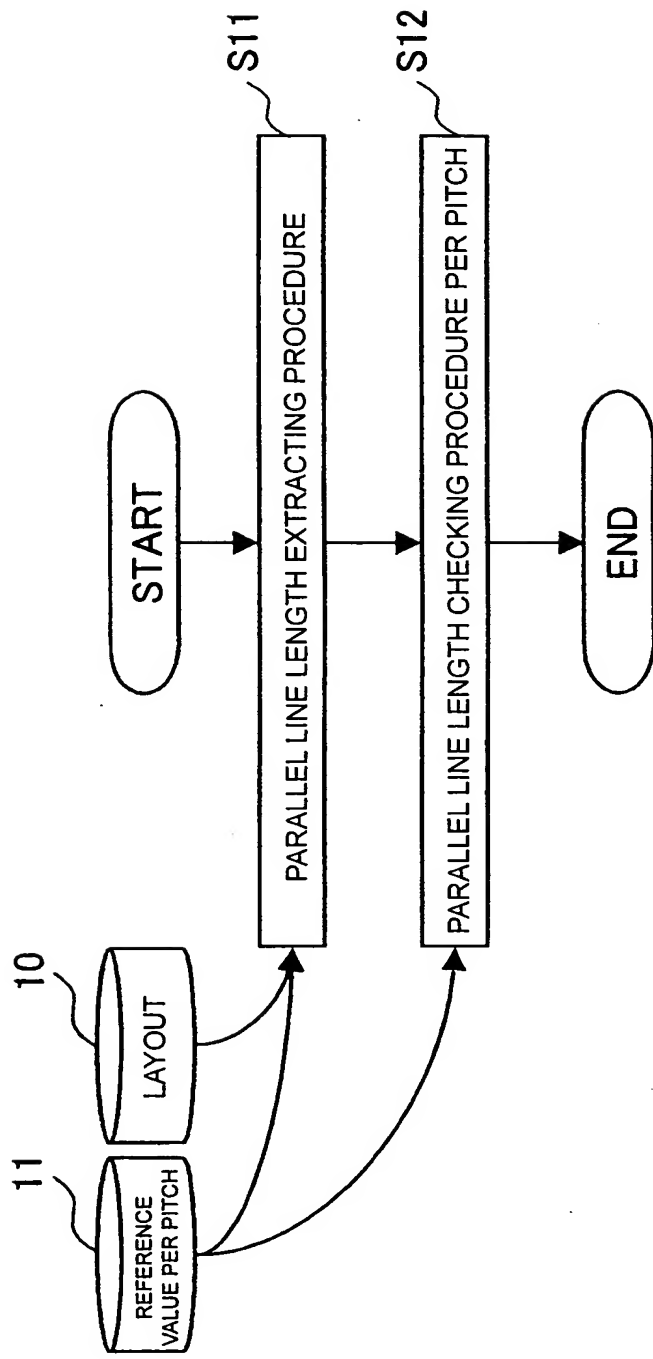


FIG. 2A

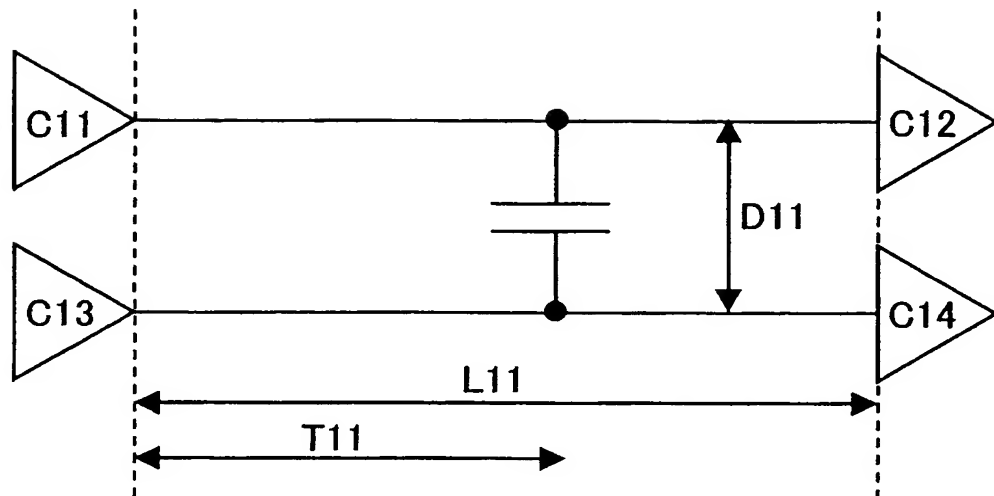


FIG. 2B

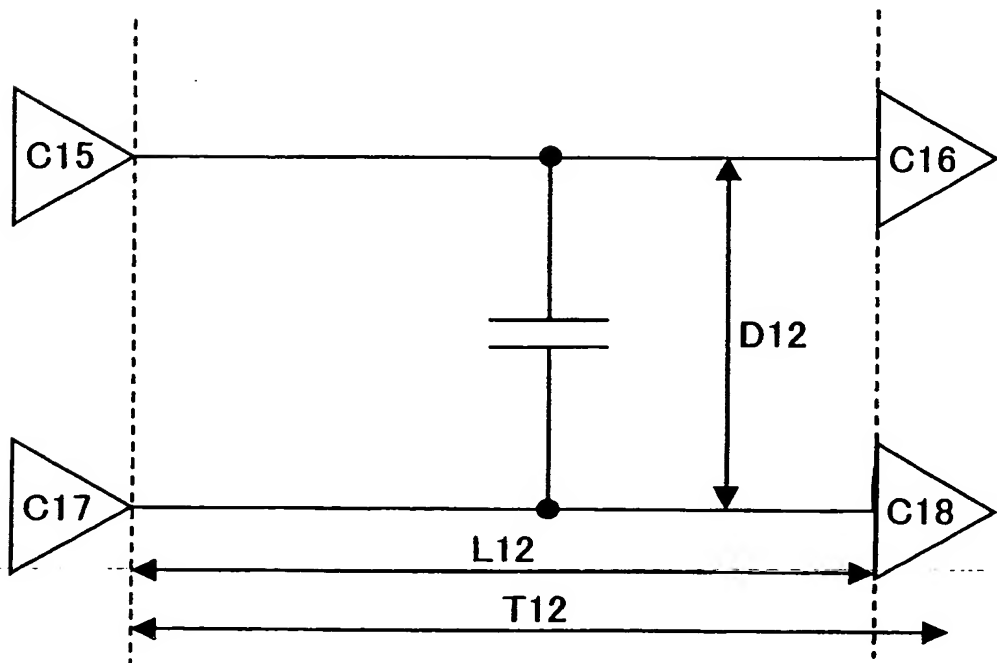


FIG. 3

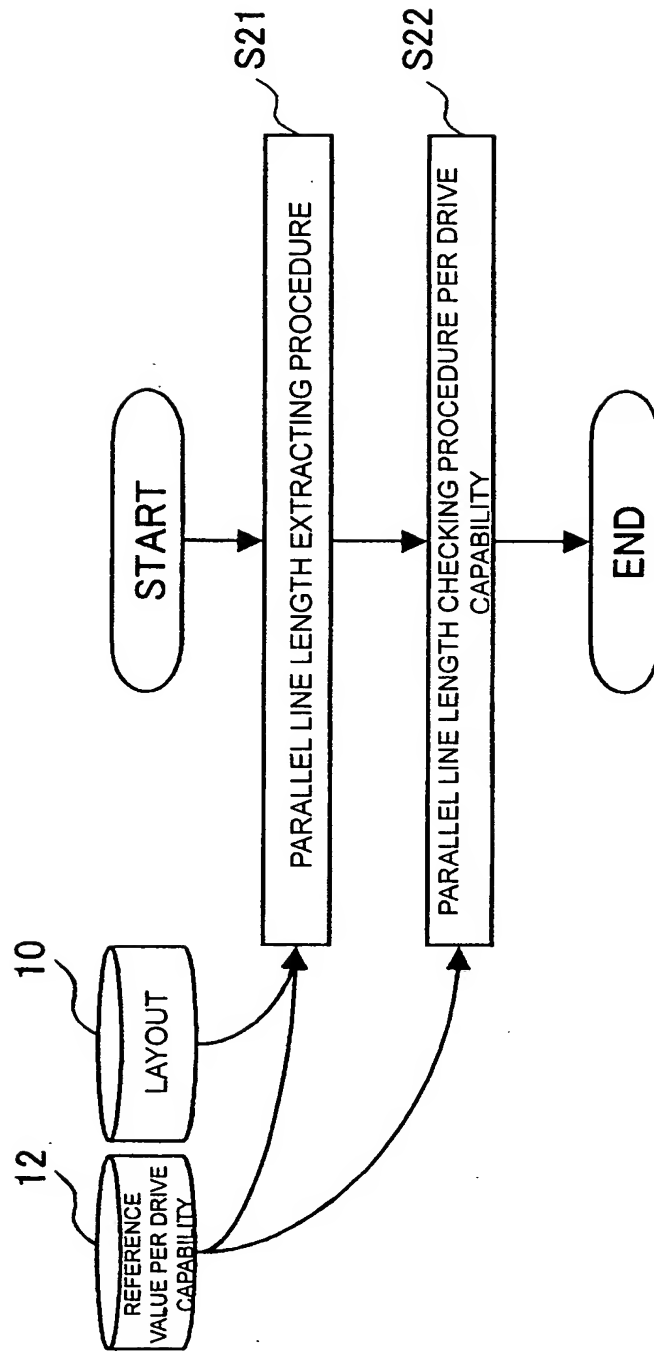


FIG. 4A

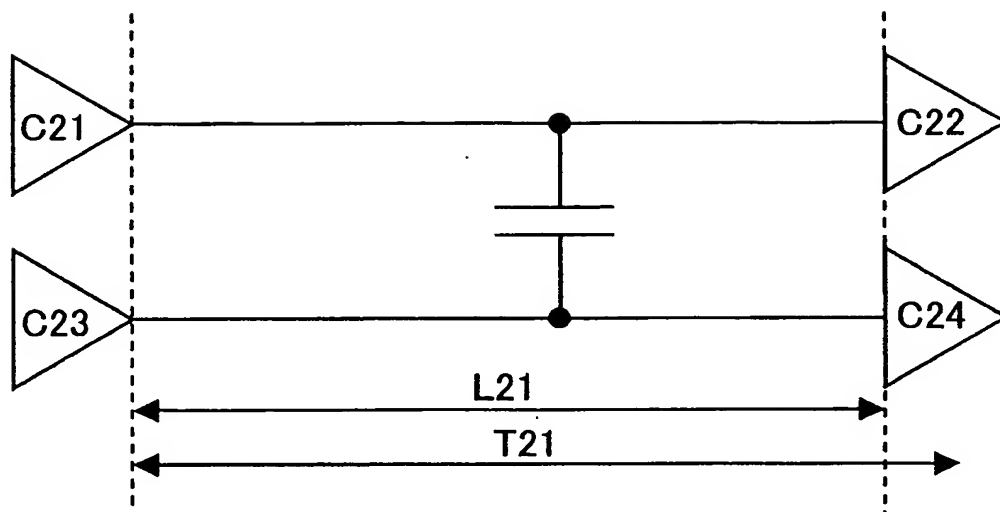


FIG. 4B

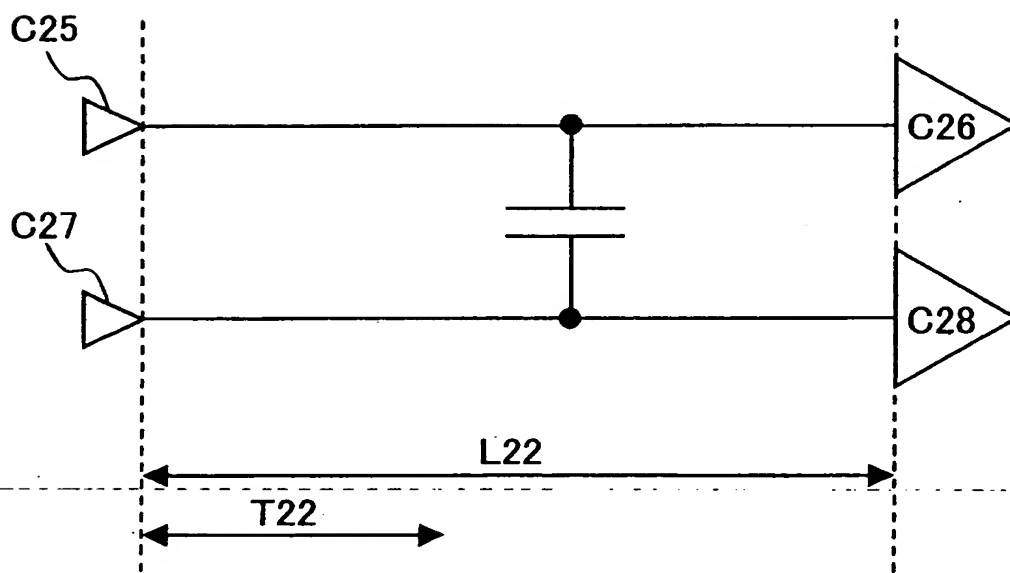


FIG. 5

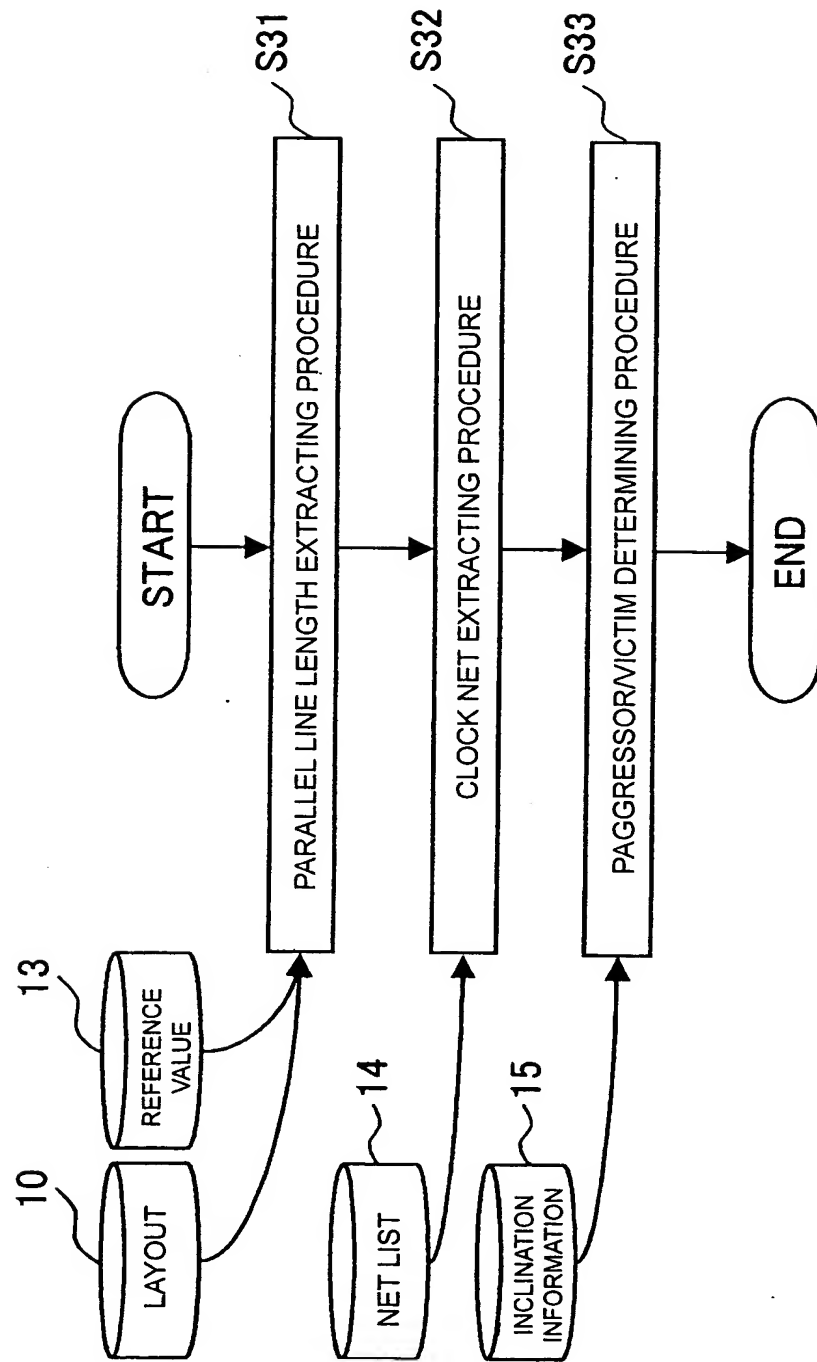


FIG. 6A

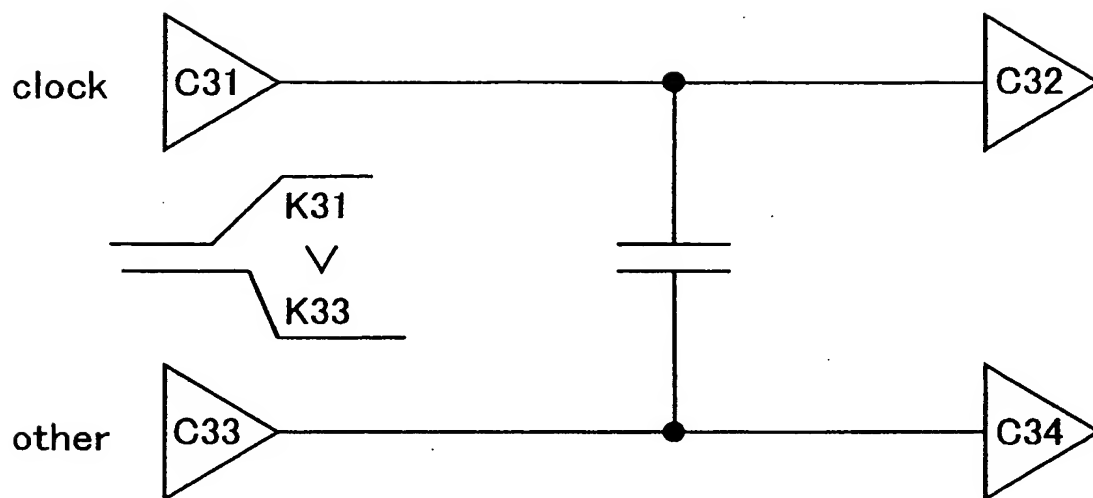


FIG. 6B

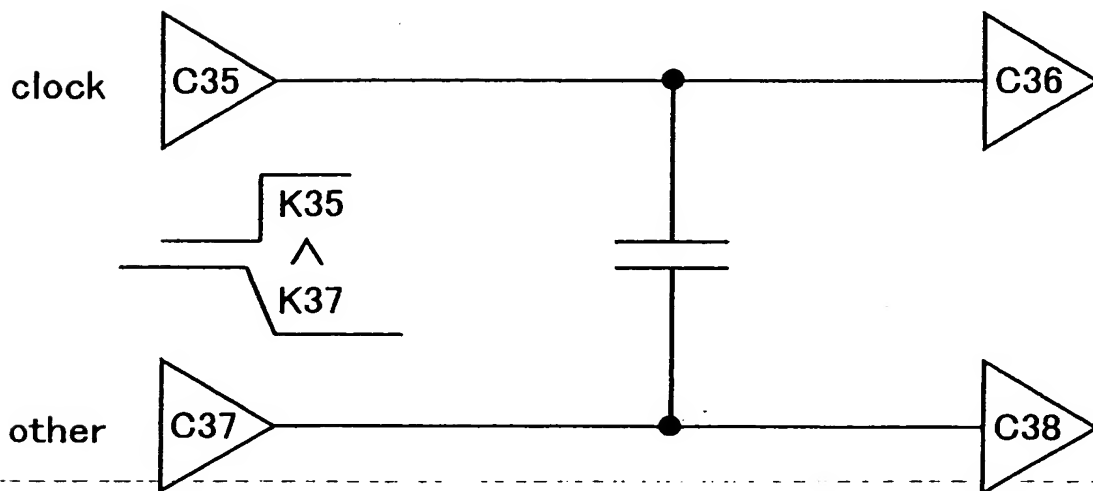


FIG. 7

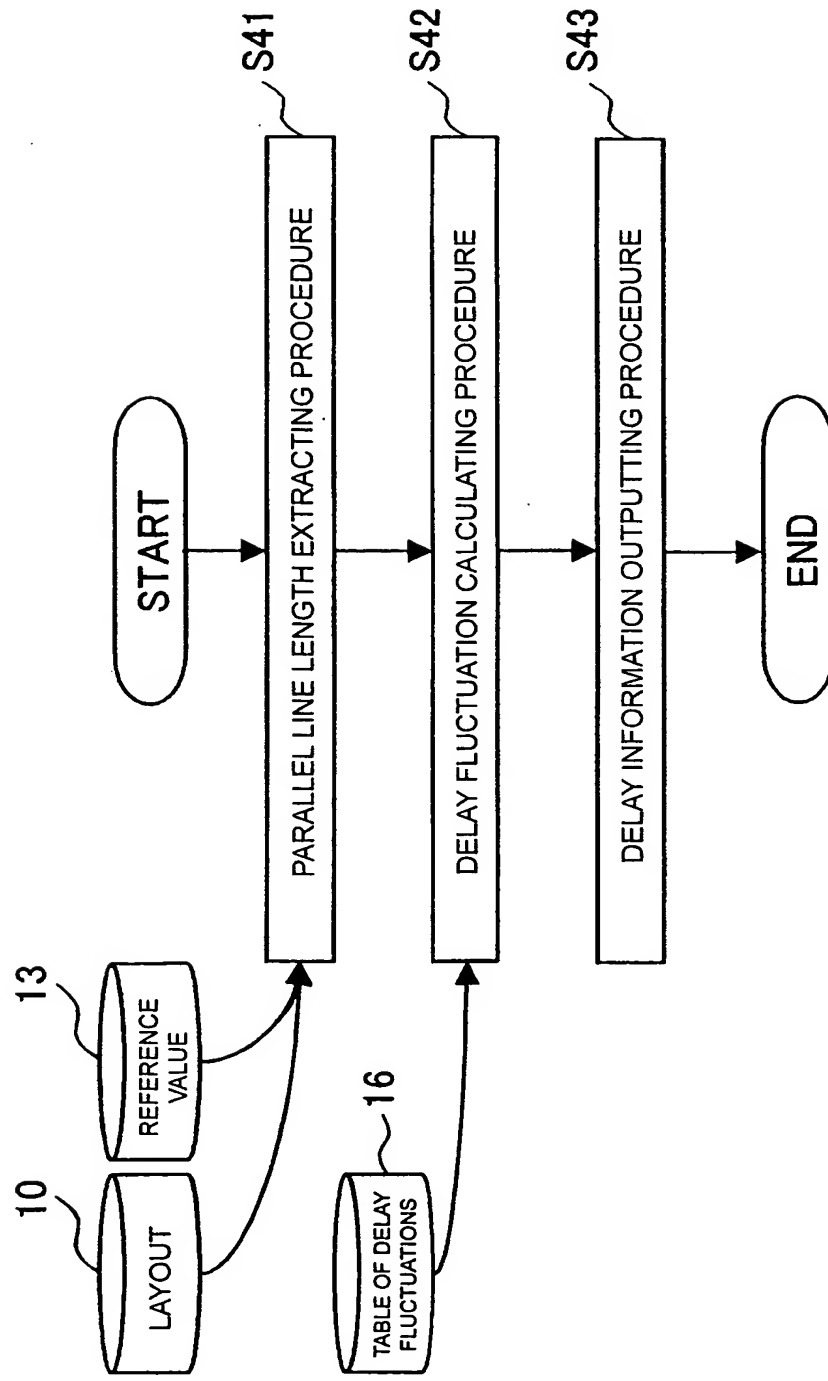


FIG. 8A

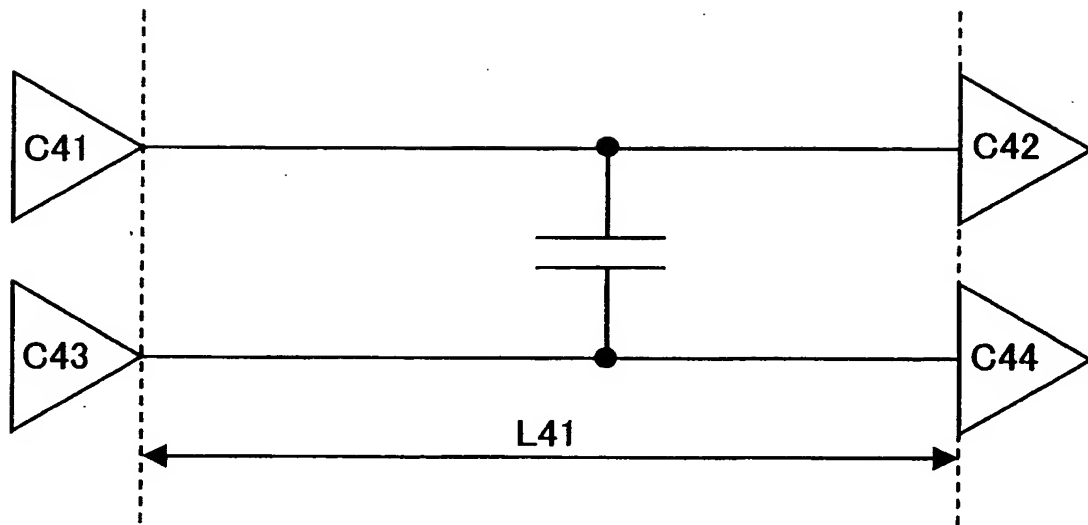


FIG. 8B

		Parallel line length			
		100 μ m	200 μ m	500 μ m	1000 μ m
Drive capability	x1	160ps	320ps	800ps	1600ps
	x2	80ps	160ps	400ps	800ps
	x4	40ps	80ps	200ps	400ps
	x6	20ps	40ps	100ps	200ps
	x8	10ps	20ps	50ps	100ps

Delay fluctuation \sim 17
160ps

FIG. 9

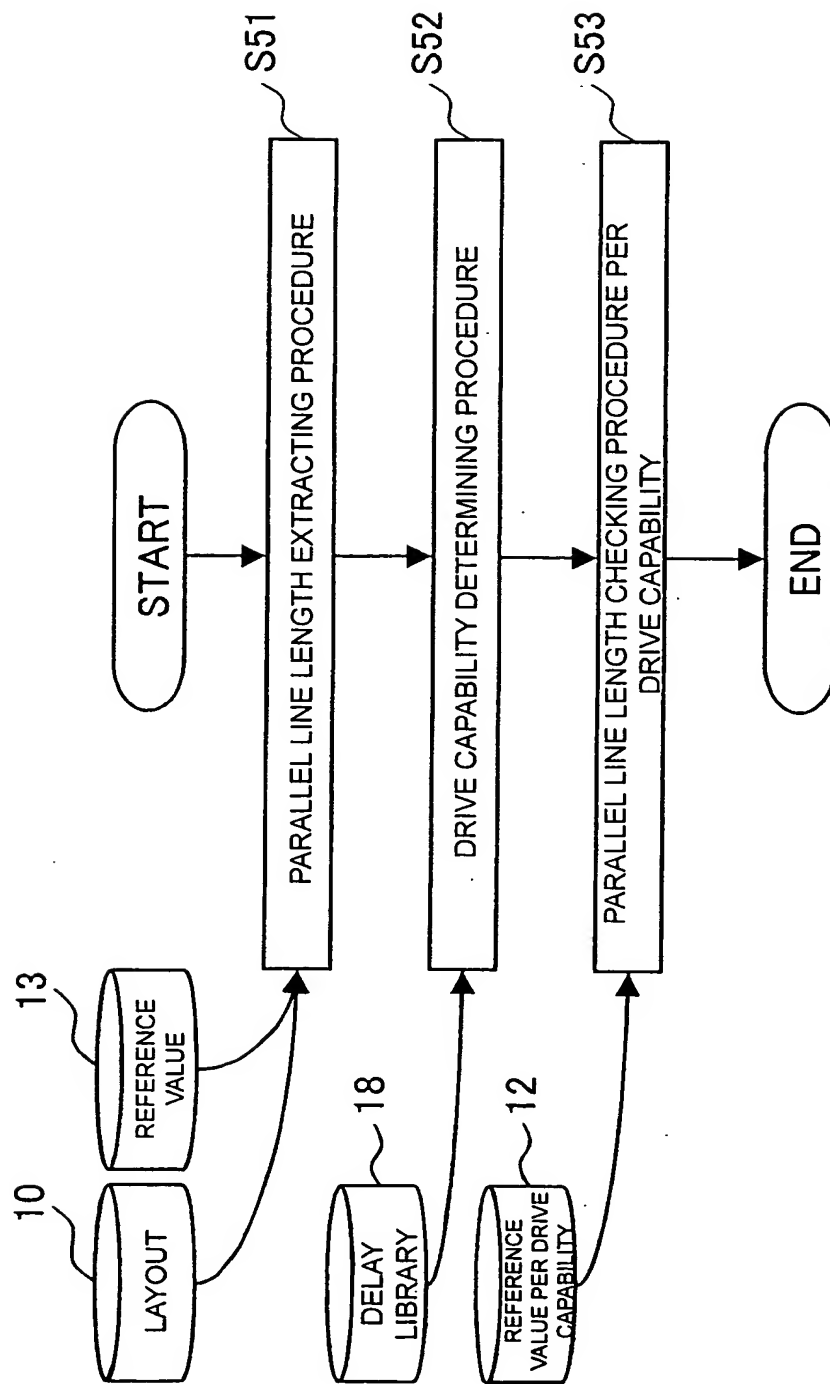


FIG. 10

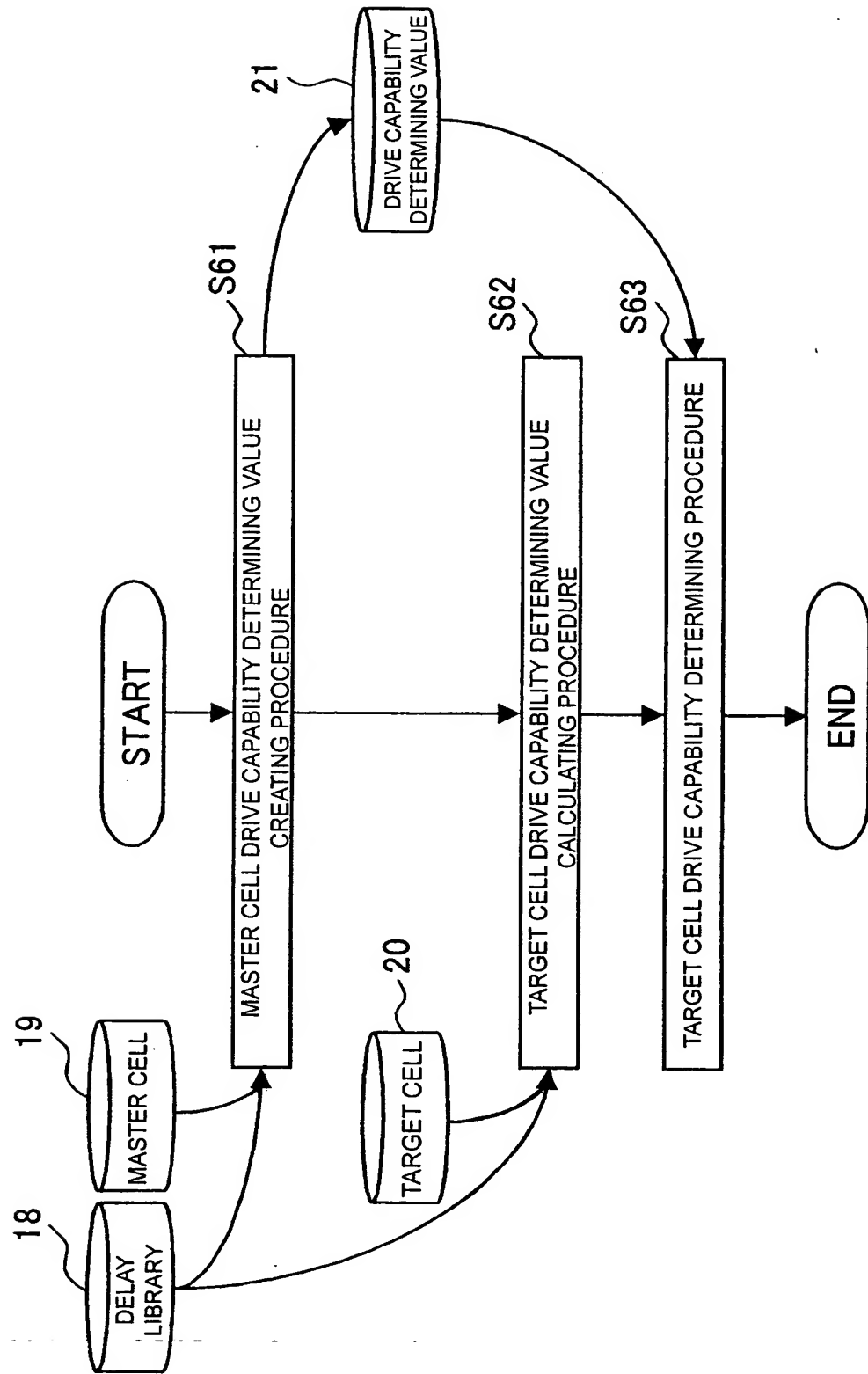


FIG. 11

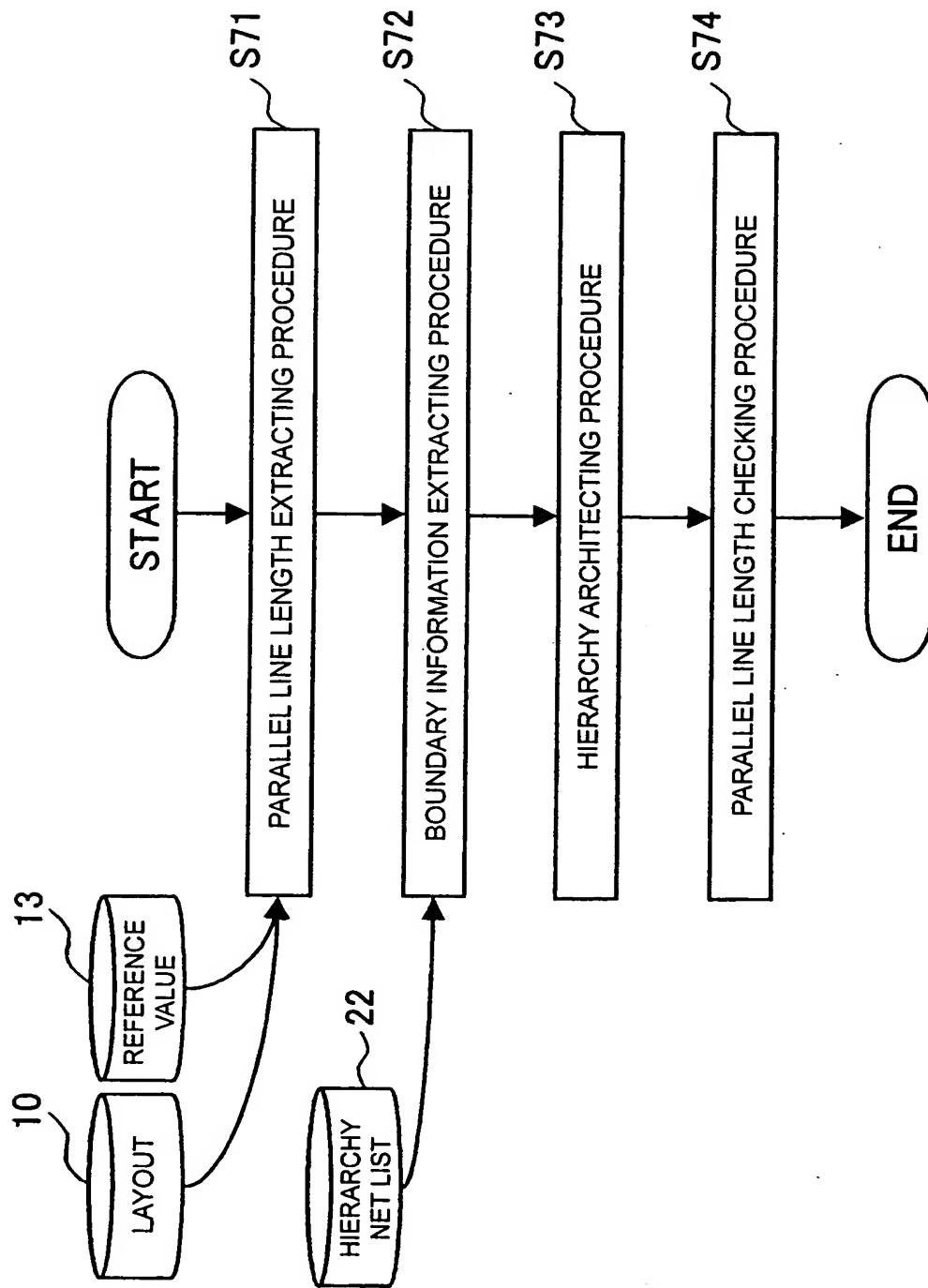


FIG. 12A

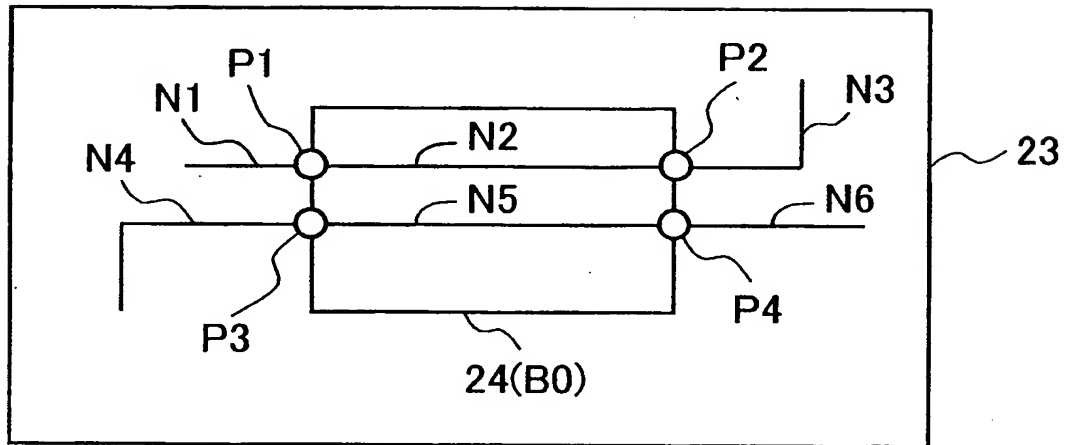
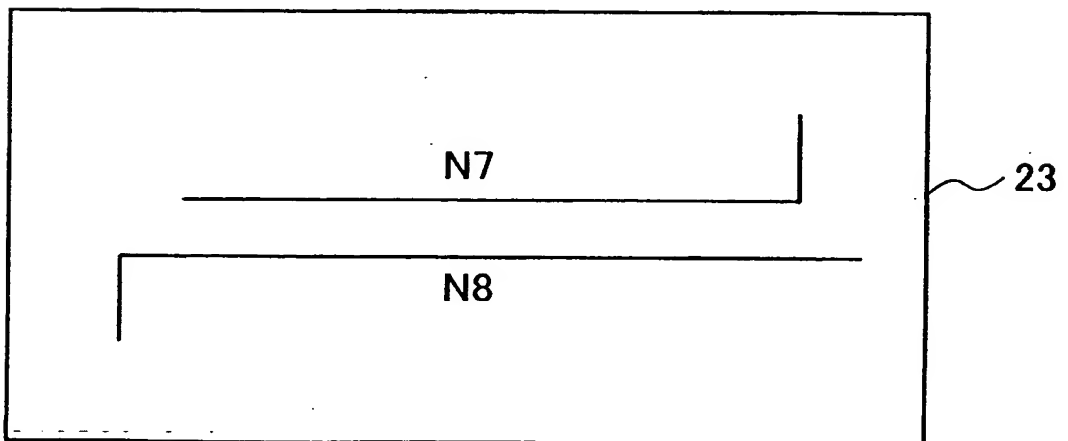


FIG. 12B



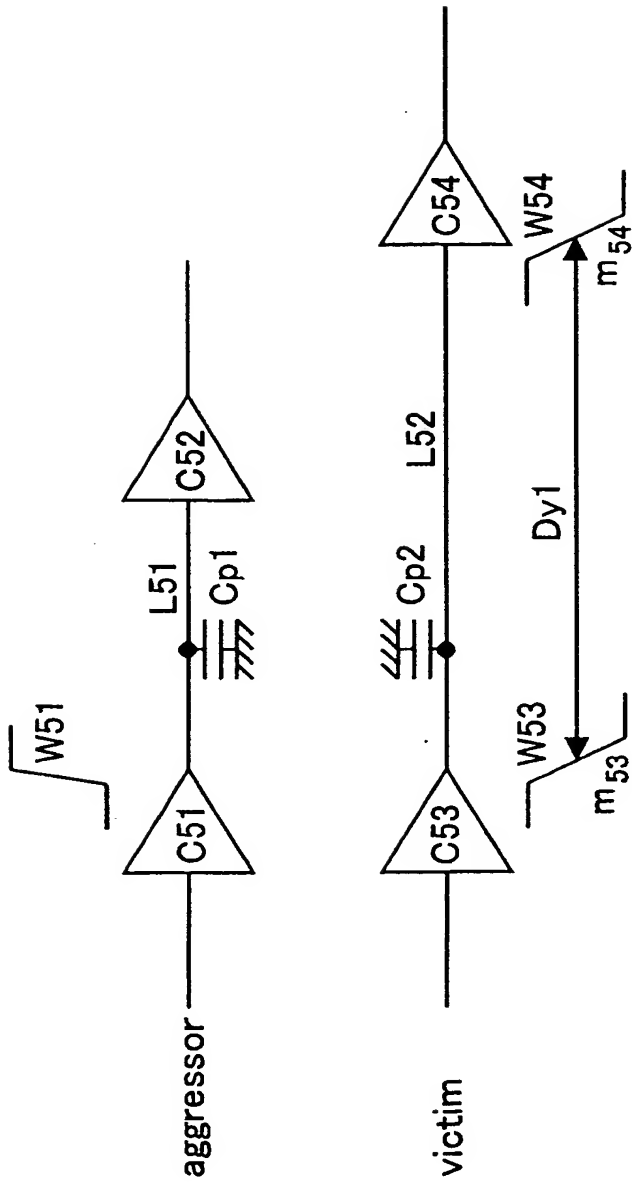


FIG. 13A

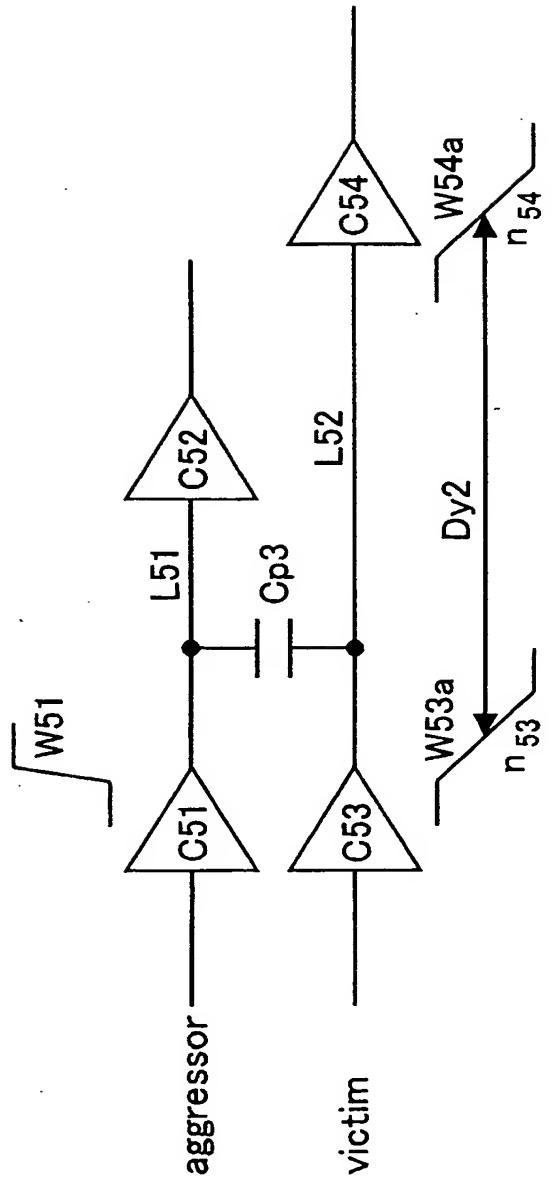


FIG. 13B

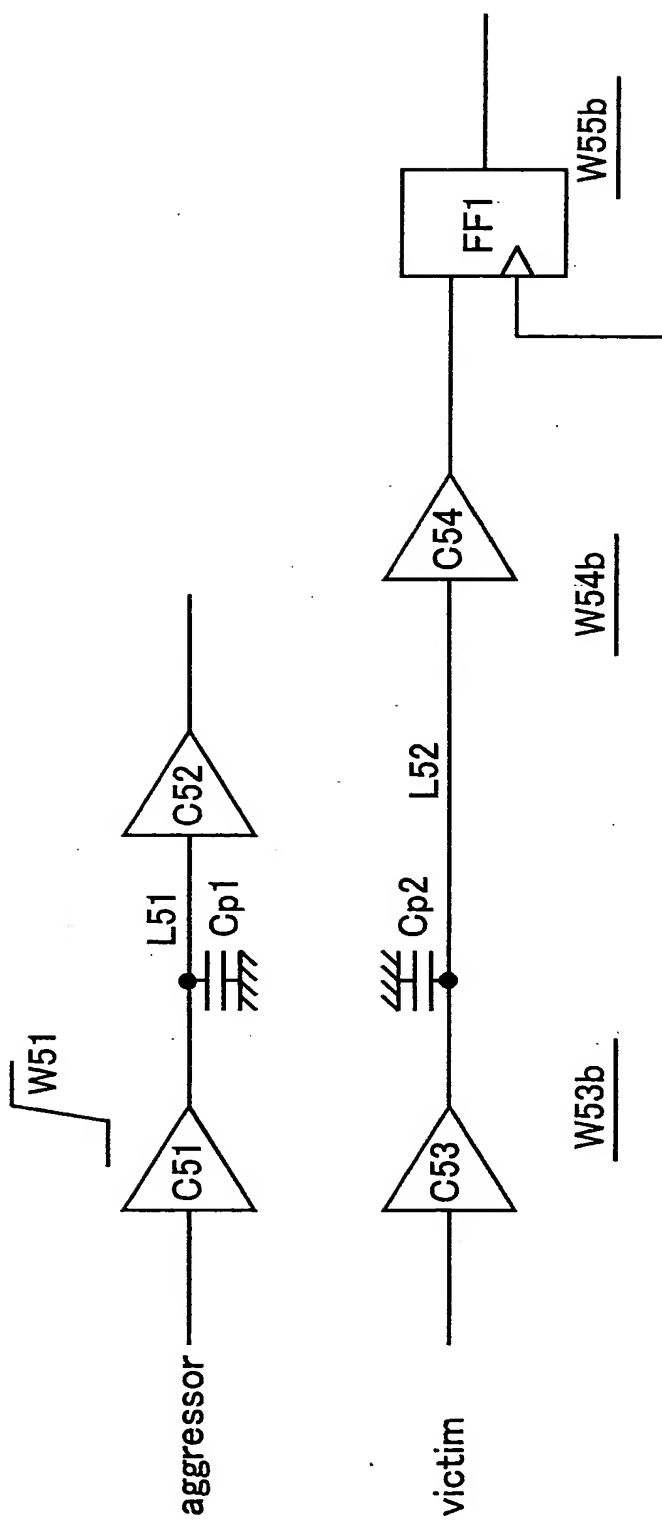


FIG. 14A

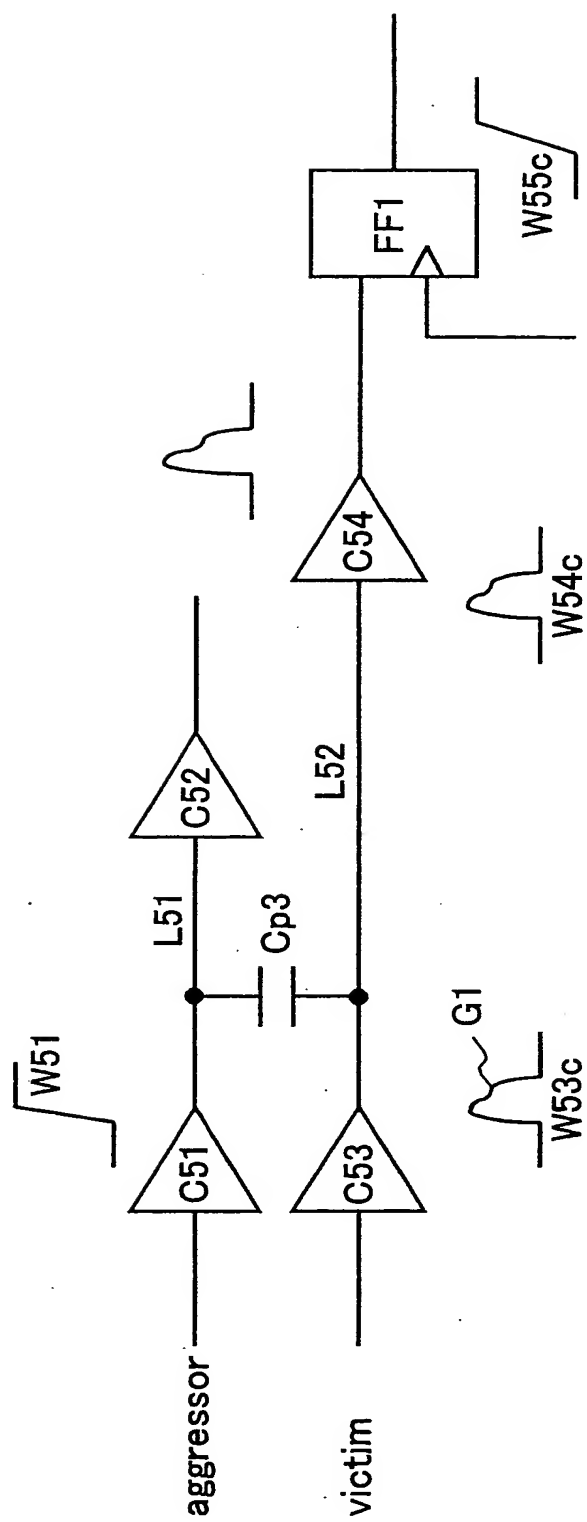


FIG. 14B

FIG. 15

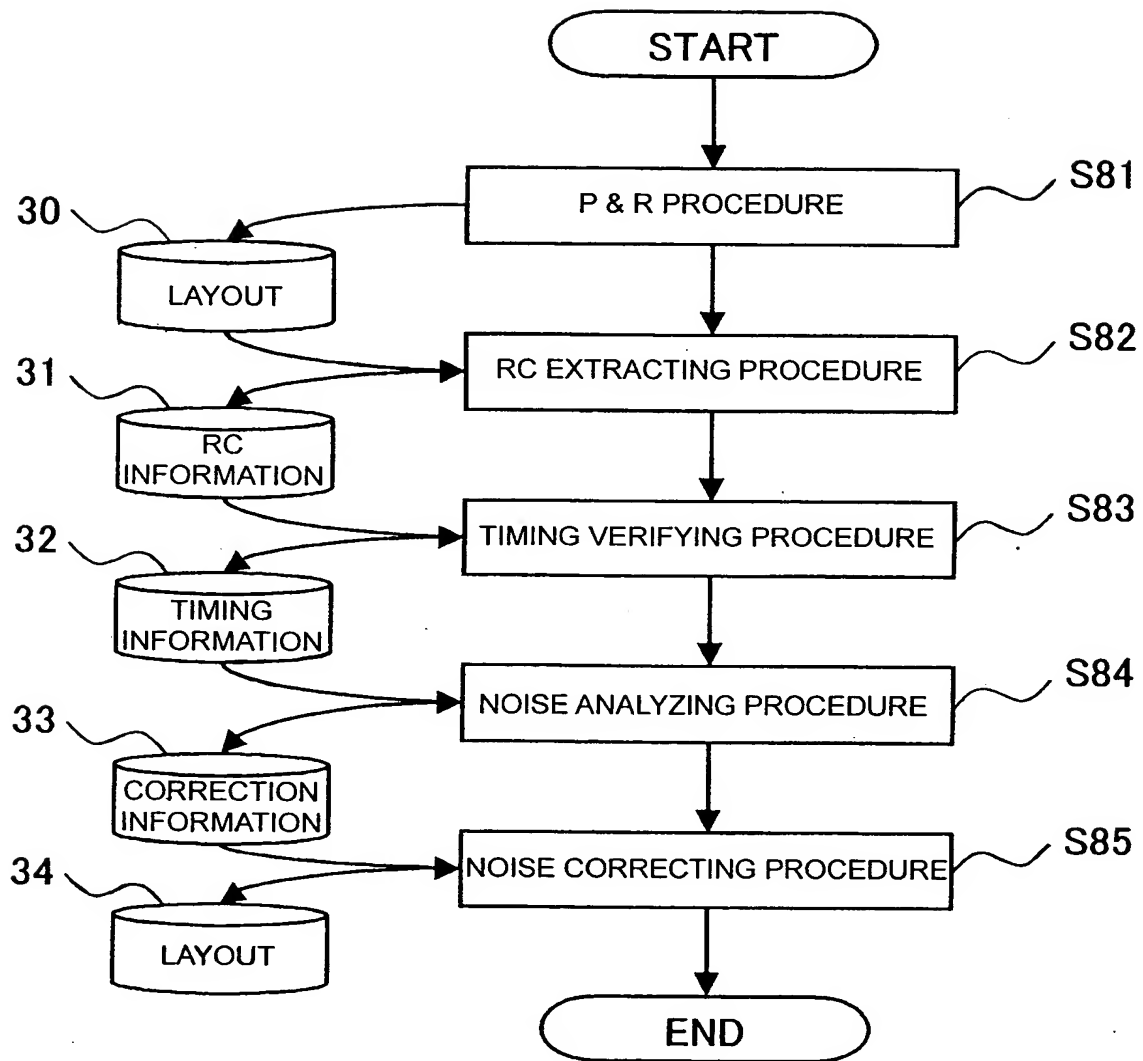


FIG. 16

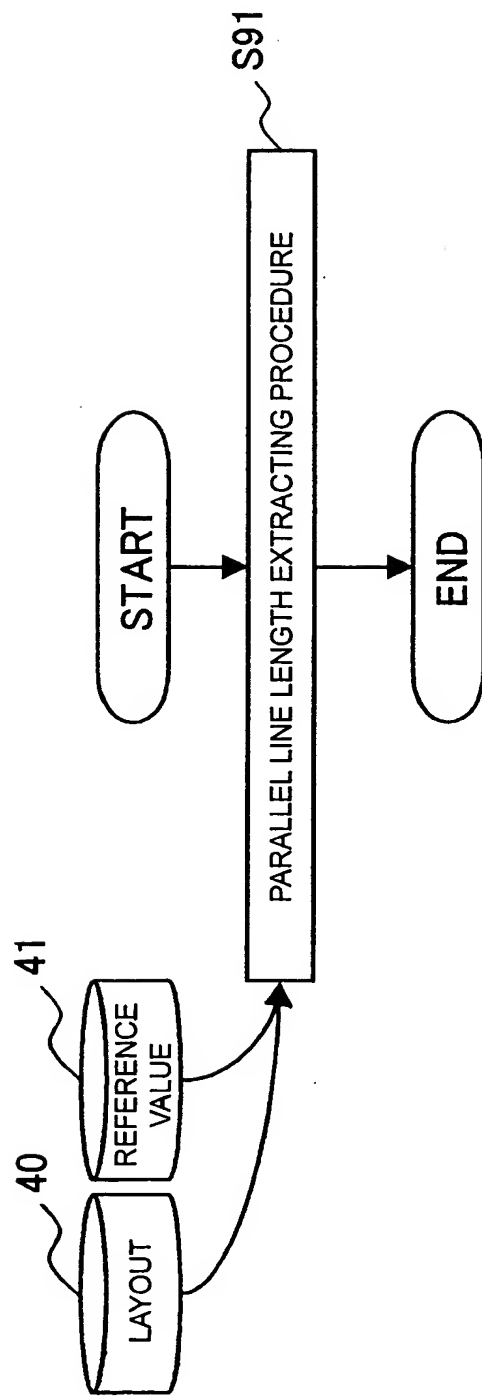


FIG. 17

